



MWT-0618S-2P2/0618Z-2P2

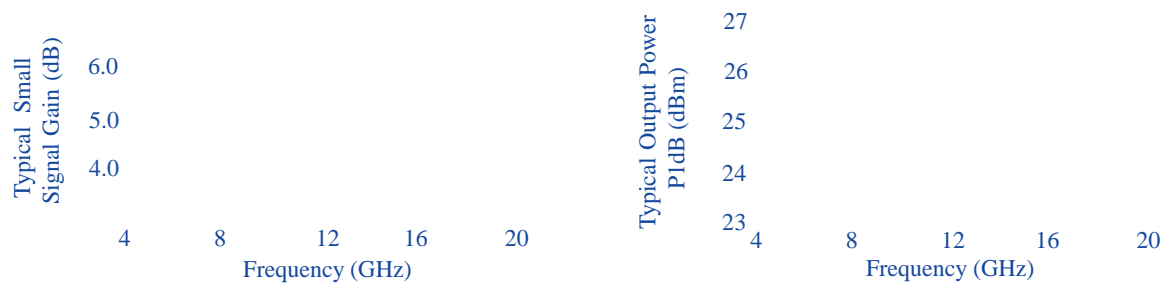
6.0-18.0 GHz Balanced Amplifier Module

www.mwtinc.com

Email: info@mwtinc.com

TYPICAL SPECIFICATIONS AT 25 °C

- 25.5 dBm P-1dB
- 5.3 dB SMALL SIGNAL GAIN
- 15.0 dB INPUT/OUTPUT RETURN LOSS
- 220 mA @ +8V
- USES TWO MWT-2HP GaAs FET DEVICES
- 7.0 dB NOISE FIGURE



ELECTRICAL SPECIFICATIONS (Ta=25°C)

| SYMBOL | PARAMETERS | UNITS | MIN | TYP | MAX |
|---------------------|-----------------------------------|-------|------|--------|-------|
| FREQ | Frequency Range | GHz | 6.0 | | 18.0 |
| SSG | Small Signal Gain | dB | 5.0 | 5.3 | |
| $\Delta G/\Delta F$ | SSG Flatness | +/-dB | | 0.3 | 0.5 |
| $\Delta G/\Delta T$ | SSG Variation over Temperature | dB/°C | | -0.12 | |
| P-1dB | Output Power at 1dB Compression | dBm | 24.5 | 25.5 | |
| PSAT | Output Power at 6 dB Compression | dBm | | 26.0 | |
| $\Delta P/\Delta T$ | P-1dB Variation over Temperature | dB/°C | | -0.008 | |
| IP3 | Third Order Intercept Point | dBm | | 36.0 | |
| 2nd HAR | 2nd Harmonic at Pout=25.5 dBm | dBc | | -21.0 | |
| 2nd HAR | 2nd Harmonic at Pout=26.0 dBm | dBc | | -16.0 | |
| NF | Noise Figure | dB | | 7.0 | |
| VSWR IN | Input VSWR | ---- | | 1.5:1 | 1.7:1 |
| VSWR OUT | Output VSWR | ---- | | 1.5:1 | 1.7:1 |
| ISO | Reverse Isolation | dB | | -20.0 | |
| VDD | Power Supply Voltage | +V | 7.9 | 8.0 | 8.1 |
| IDD | Small Signal Module Current | mA | | 220.0 | 275.0 |
| RTH | Thermal Resistance Including FET* | °C/W | | 96.0 | |

* When calculating Tch, use FET Vds = 6.0 volts and FET Ids = 110mA

4268 Solar Way • Fremont • California 94538 • Phone: (510) 651-6700 • Fax: (510) 651-2208

All rights reserved. MicroWave Technology, Inc. All specifications subject to change without notice.

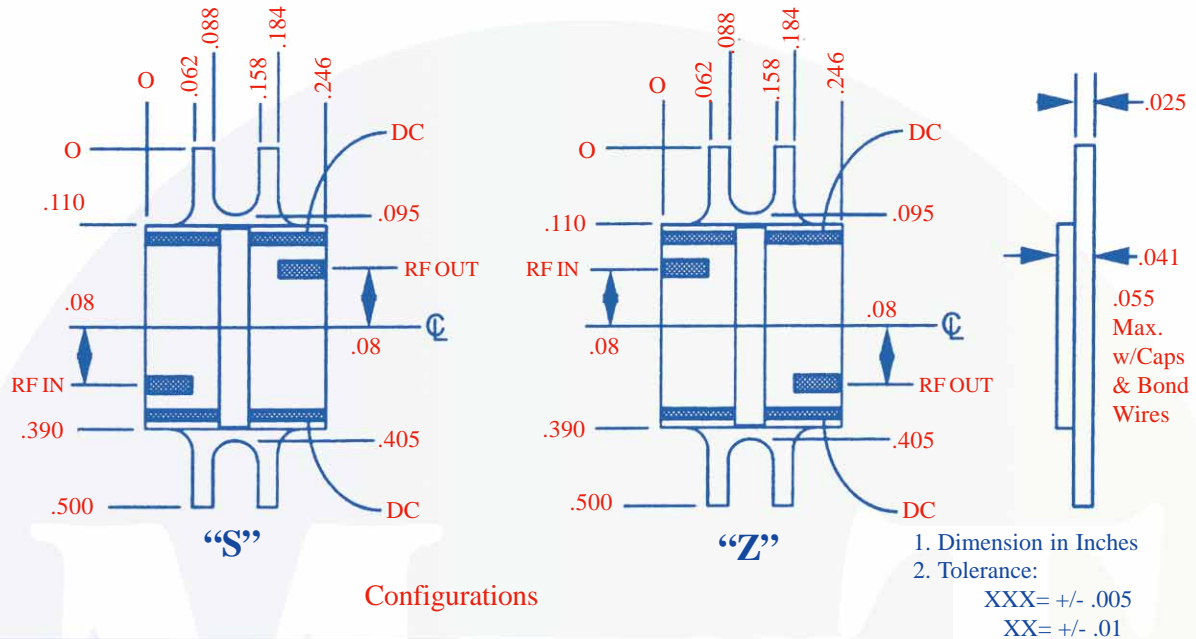


MWT-0618S-2P2/0618Z-2P2

6.0-18.0 GHz Balanced Amplifier Module

www.mwtinc.com

Email: info@mwtinc.com



Configurations

Construction:

The 15 mil alumina substrates and 10 mil copper FET ridge are brazed onto the 25 mil carrier using AuGe preform. The GaAs FETs are attached to the Cu ridge using AuSn preform. All capacitors are attached using AuSn preforms. The flanges are designed to accommodate 0-80 UNF-2A socket or Fillister head screws on .400 center-to-center hole spacing. The modules are mechanically and electrically designed to be cascaded.

Notes:

1. Custom module specifications and/or custom module mechanical configurations are available.
2. Operating Temperature Range is -55 degrees Celsius to +105 degrees Celsius.
3. All modules are serialized and shipped with data measured at 25 degrees Celsius. Data includes swept small signal gain, swept input and output return loss. Noise figure and P-1dB are measured in 1 GHz increments. Special module testing is available.
4. Test Fixtures are available.
5. Microwave Technology reserves the right to ship modules with performance above the typical specifications.

4268 Solar Way • Fremont • California 94538 • Phone: (510) 651-6700 • Fax: (510) 651-2208

All rights reserved. MicroWave Technology, Inc. All specifications subject to change without notice.