Features:

- 45 dBm IP3
- 13 dB Gain
- +28.0 dBm P1dB
- Single Positive Bias
- Leadless Surface Mount Package
- EVM < 1.0% at 20 dBm

Description:

The WPS-242717-02 is a low cost high linearity modular amplifier designed to meet the ultra-linear transmitter driver requirements for 802.16 WiMax linear driver applications and commercial 2G, 2.5G, 3G, GSM, TDMA, EDGE, UMTS, WCDMA, CDMA2000, and TD-SCDMA applications. Key advantages are low EVM performance for 802.16 WiMax applications and low intermodulation performance for multi-carrier and CDMA systems together with exceptionally low input/output return loss for ease of integration.

Electrical Specifications:

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETERS</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Freq.</td>
<td>Frequency Range</td>
<td>2400</td>
<td>2700</td>
<td>MHz</td>
<td></td>
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<tr>
<td>SSG</td>
<td>Small Signal Gain</td>
<td>12.0</td>
<td>13.0</td>
<td>dB</td>
<td></td>
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<tr>
<td>P1 dB</td>
<td>Pout at 1 dB Comp Point</td>
<td>+28.0</td>
<td>dBm</td>
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<tr>
<td>IP3 (1)</td>
<td>Third-Order Intercept</td>
<td>42.0</td>
<td>45.0</td>
<td>dBm</td>
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<tr>
<td>VSWR</td>
<td>VSWR (Input/Output)</td>
<td>2.0:1</td>
<td></td>
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<tr>
<td>GOF</td>
<td>Gain Var. over Frequency</td>
<td>± 0.25</td>
<td>± 0.50</td>
<td>dB</td>
<td></td>
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<tr>
<td>GOT</td>
<td>Gain Var. over Temp</td>
<td>-0.015</td>
<td>dB/°C</td>
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<td>Idd</td>
<td>DC Current</td>
<td>300</td>
<td>350</td>
<td>mA</td>
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</tr>
</tbody>
</table>

(1) Two tone test @ 13 dBm/tone, centered at 2,400 MHz with 20 MHz separation.

Absolute Maximum Ratings

- Maximum Bias Voltage: 8.0 V
- Maximum continuous RF Input Power: 950 mW
- Maximum Peak Input Power: 1400 mW
- Maximum Case Operating Temperature: +85 °C
- Maximum Storage Temperature: -65 °C to +150 °C
Gain @ 25C

Return Losses @ 25C

Outline Diagram

Application Circuit

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Typical Performance at 25°C (measurements taken in production test fixture)
The test signal is downlink WiMAX 802.16d and includes the preamble, FCH and QPSK, 16 QAM and 64 QAM. The demodulator is programmed to measure the output power and EVM for 64 QAM. The EVM error is the accumulated error from the modulator and WPS amplifier. The EVM floor for the modulator is 0.4%. At 19 dBm the WPS amplifier contributes less than 1% EVM.

IEEE 802.16 - 2004

Frequency: 2.5 GHz
Signal Level: 17.9 dBm
External Att: 0 dB
Sweep Mode: Continuous
Trigger Mode: Power
Trigger Offset: -10 µs
Burst Type: OTDM DL Burst
Modulation: 64QAM3/4
No Of Data Symbols: 1/2425

Capture Memory
No of Samples: 480001
Capture Time: 15 ms
Gate: 100 µs - 500 µs
Marker 1
Ref: 27.9 dBm
Att(E1): 40.00 / 0.00 dB
Burst: 2 (0) 0 s

Constellation vs Symbol
Marker 1
Quadrature: 0.0247
Inphase: 0.4172