MMA-374030-R5

37-41GHz, 1W MMIC Power Amplifier
Data Sheet

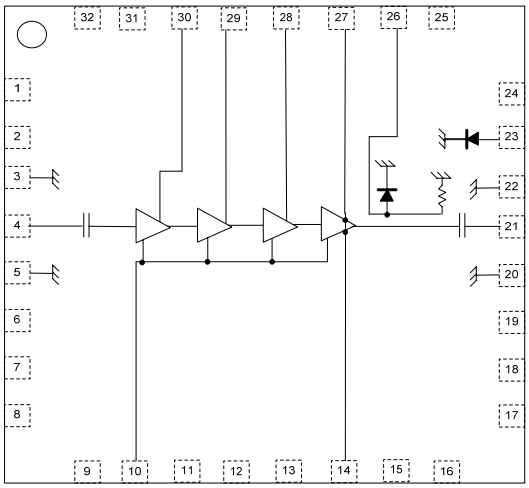
Old package not recommended for new designs

Features:

- **Frequency Range: 37 - 41 GHz**
- **P1dB: +30.5 dBm**
- **IM3 Level: -41 dBc @Po=18dBm/tone**
- **Gain: 22 dB**
- **Vdd = 4 to 6 V**
- **Idsq = 1000 to 2000 mA**
- **Input and Output Fully Matched to 50 Ω**
- **Integrated power detector**

Applications:

- **P2P Radio**
- **V-sat**



Functional Block Diagram

Description:

The MMIC is an OIP3=39dBm high linearity power amplifier in a surface mount package designed for use in transmitters that operate at frequencies between 37GHz and 41GHz. In the operational frequency band, it provides 30.5dBm of output power (P-1dB) and 22dB of small-signal gain. This PA is also designed for high linearity applications, and the PA shows better than -41dBc of IM3 level at 18dBm/tone output power level.

Absolute Maximum Ratings: *(Ta= 25 ℃)**

SYMBOL	PARAMETERS	UNITS	Min.	Max.
Vds	Drain-Source Voltage	V		6.5
Vg	Gate-Source Voltage	V	-2.1	0
Ig	First Gate Current	mA	-11	11
Pd	Power Dissipation	W		11.2
Pin max	RF Input Power	dBm		20
Toper	Operating Temperature	ºC		-40 to +85
Tch	Channel Temperature	ºC		+150
Tstg	Storage Temperature	ºC		-55 to +150
Tmax	Max. Assembly Temp (20 sec max)	ºC		+250

*Operation of this device above any one of these parameters may cause permanent damage.

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Electrical Specifications: *Vds=5V, Vgs=0.85V, Idsq=1200mA, Ta=25 ℃ Z0=50 ohm*

Parameter	Units	Typical Data
Frequency Range	GHz	37 - 41
Gain (Typ / Min)	dB	24 / 22
Gain Flatness (Typ / Max)	+/-dB	1.5 / 1.8
Input RL(Typ/Max)	dB	10/8
Output RL(Typ/Max)	dB	10/8
Output P1dB(Typ/Min)	dBm	29/28
Output P3dB(Typ/Min)	dBm	31/30.5
IM3 Level (1)	dBc	-41
Thermal Resistance	ºC/W	5.5
Operating Current at P1dB (Typ/Max)	mA	1400 / 1600

(1) Output IP3 is measured with two tones at output power of 18 dBm/tone separated by 20 MHz.

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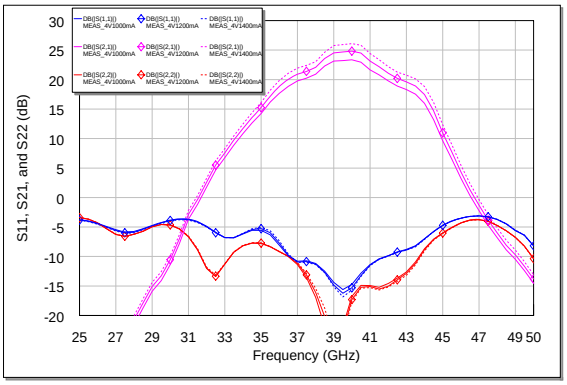
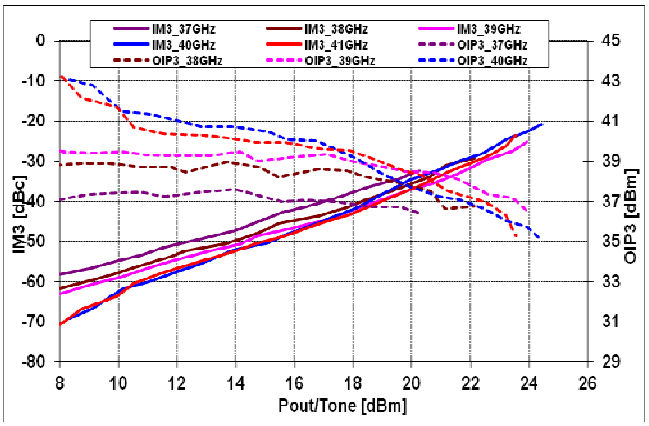
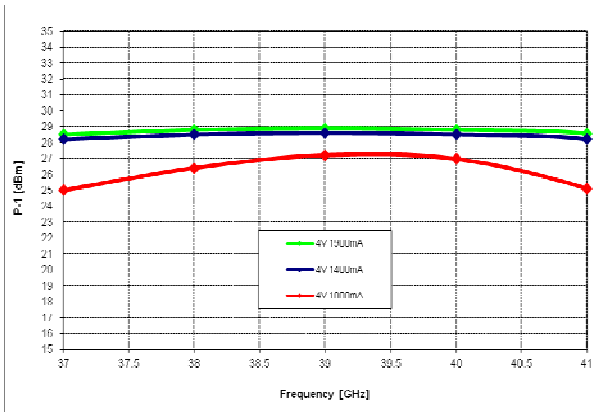
Typical Bias dependent RF Performance: Vds=4V



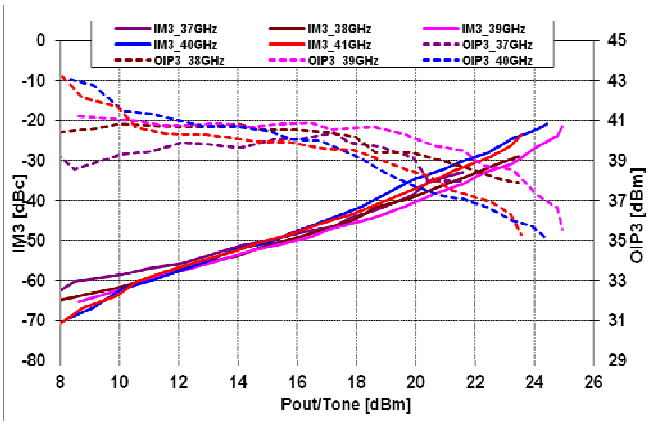
SS performance vs. Frequency (Ids=1, 1.2, and 1.4A)



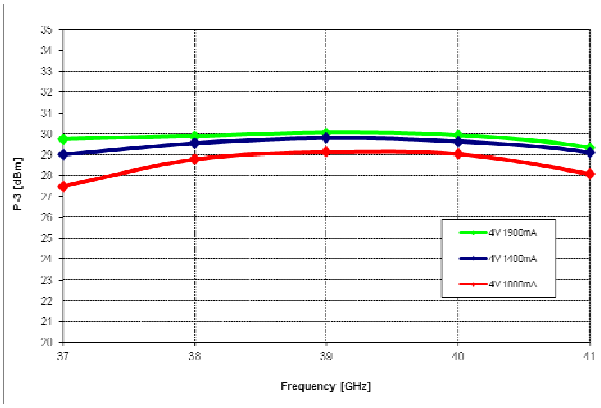
@Vds=4V, Idsq=1000mA



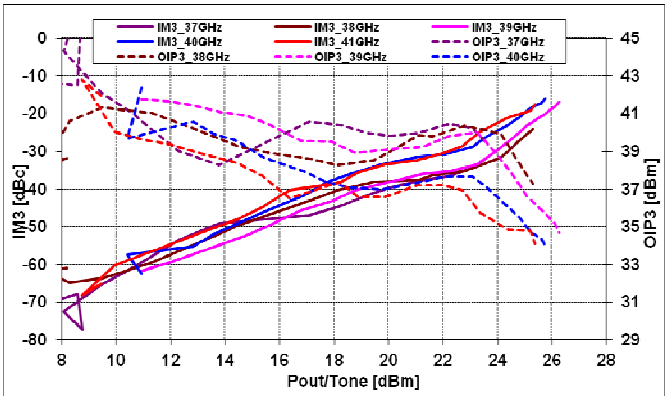
Bias dependent P-1 vs. Frequency



@Vds=4V, Idsq=1200mA



Bias dependent P-3 vs. Frequency



@Vds=4V, Idsq=1600mA

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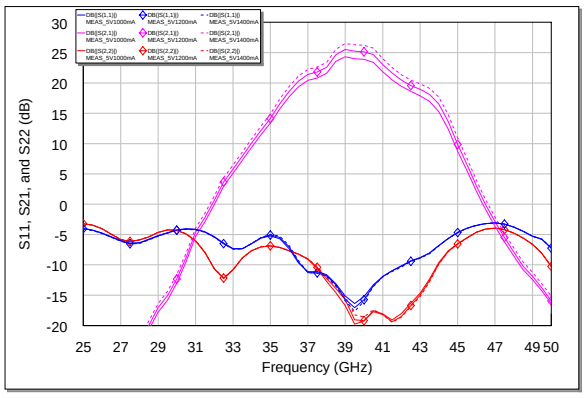
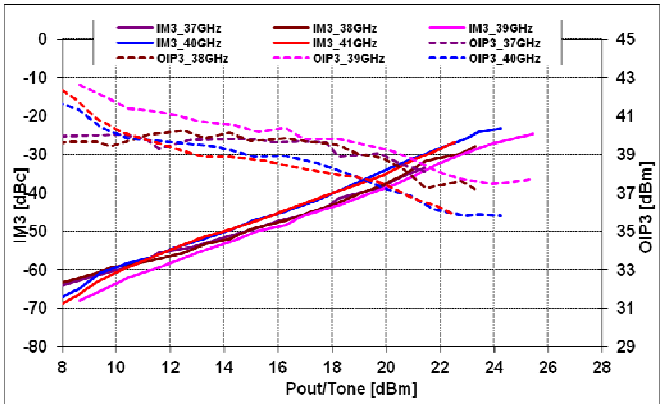
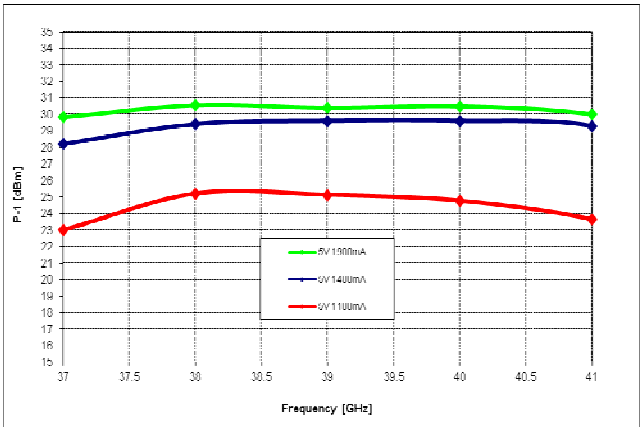
Typical Bias dependent RF Performance: Vds=5V



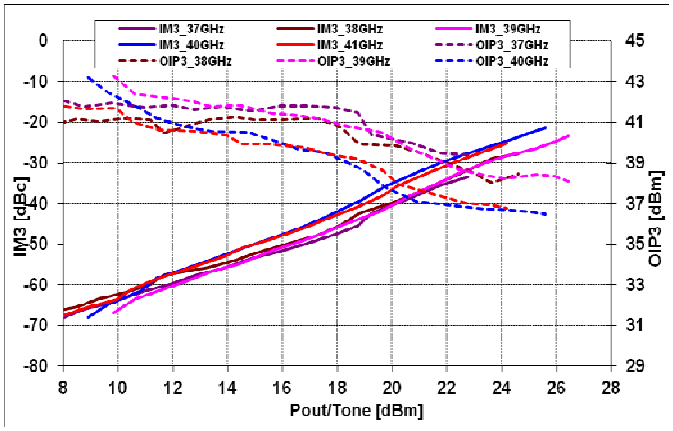
SS performance vs. Frequency (Ids=1, 1.2, and 1.4A)



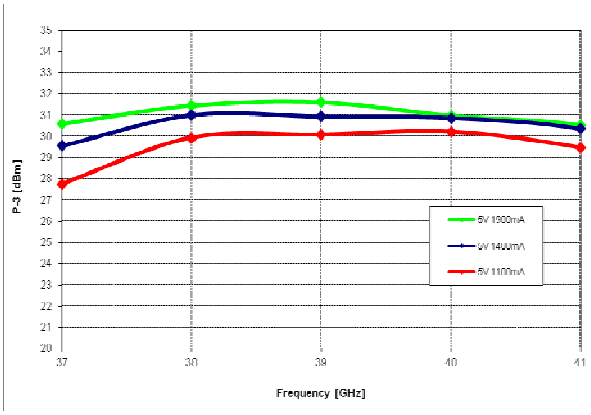
@Vds=5V, Idsq=1000mA



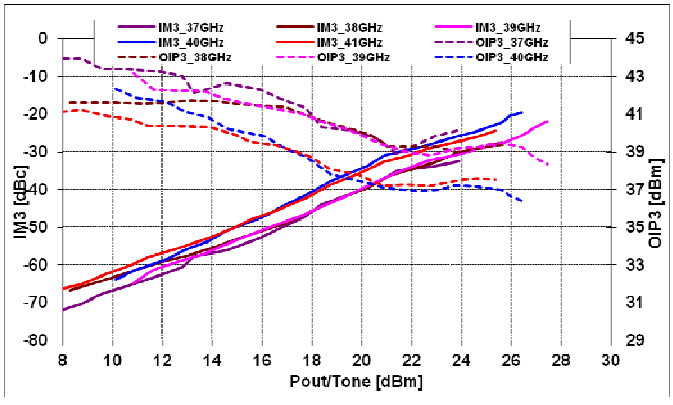
Bias dependent P-1 vs. Frequency



@Vds=5V, Idsq=1200mA



Bias dependent P-3 vs. Frequency



@Vds=5V, Idsq=1500mA

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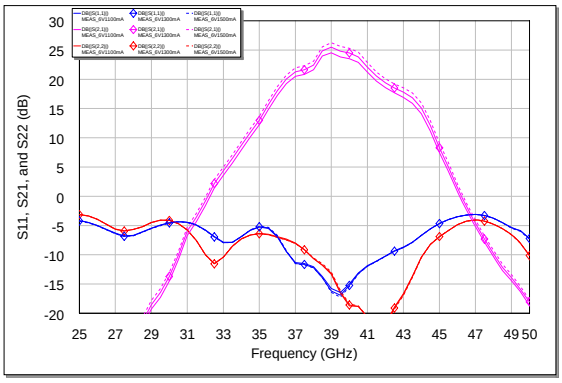
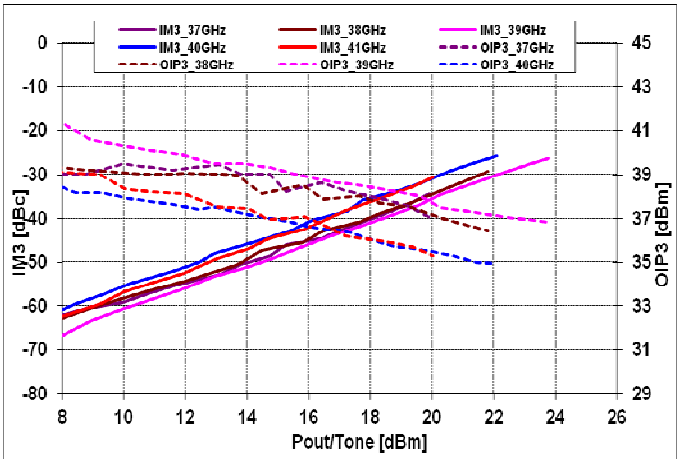
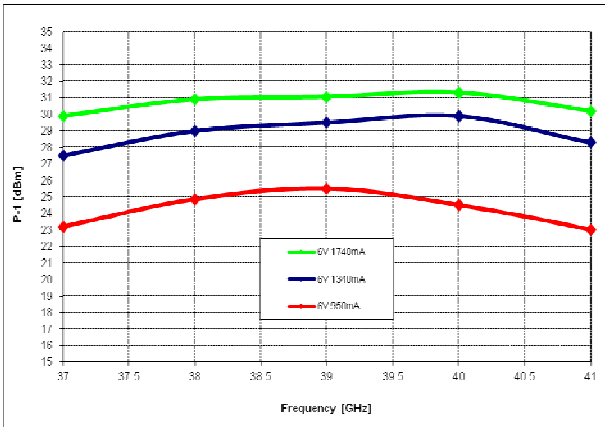
Typical Bias dependent RF Performance: Vds=6V



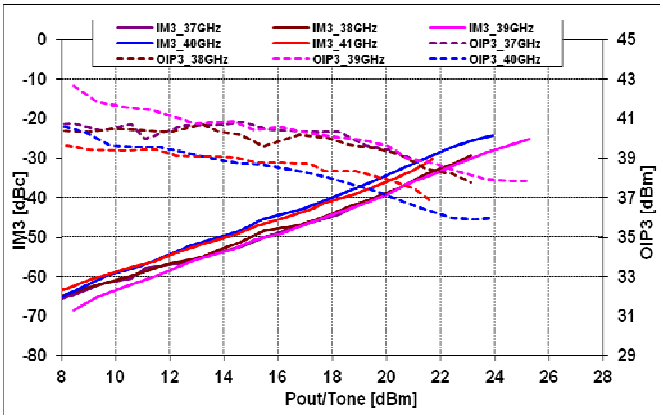
SS performance vs. Frequency (Ids=1.1, 1.3, and 1.5A)



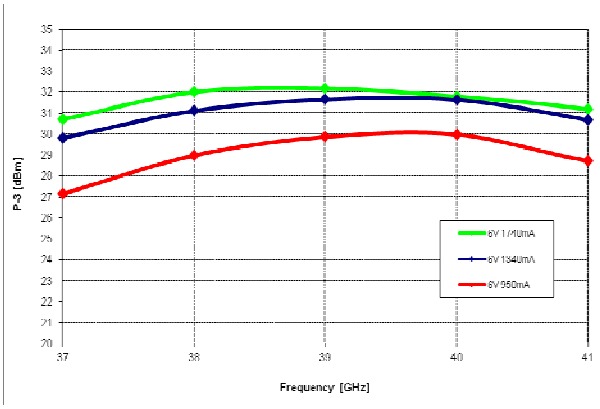
@Vds=6V, Idsq=950mA



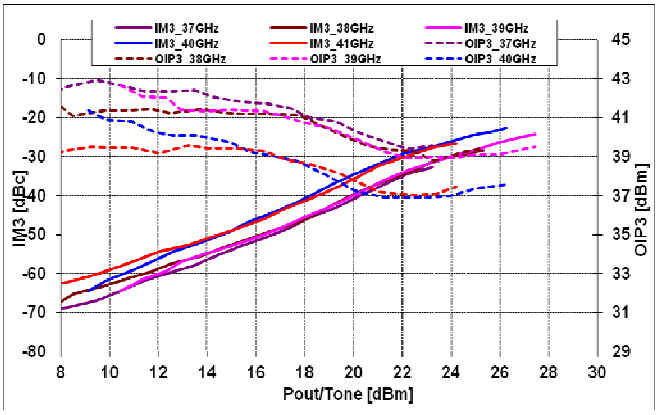
Bias dependent P-1 vs. Frequency



@Vds=6V, Idsq=1150mA



Bias dependent P-3 vs. Frequency



@Vds=6V, Idsq=1500mA

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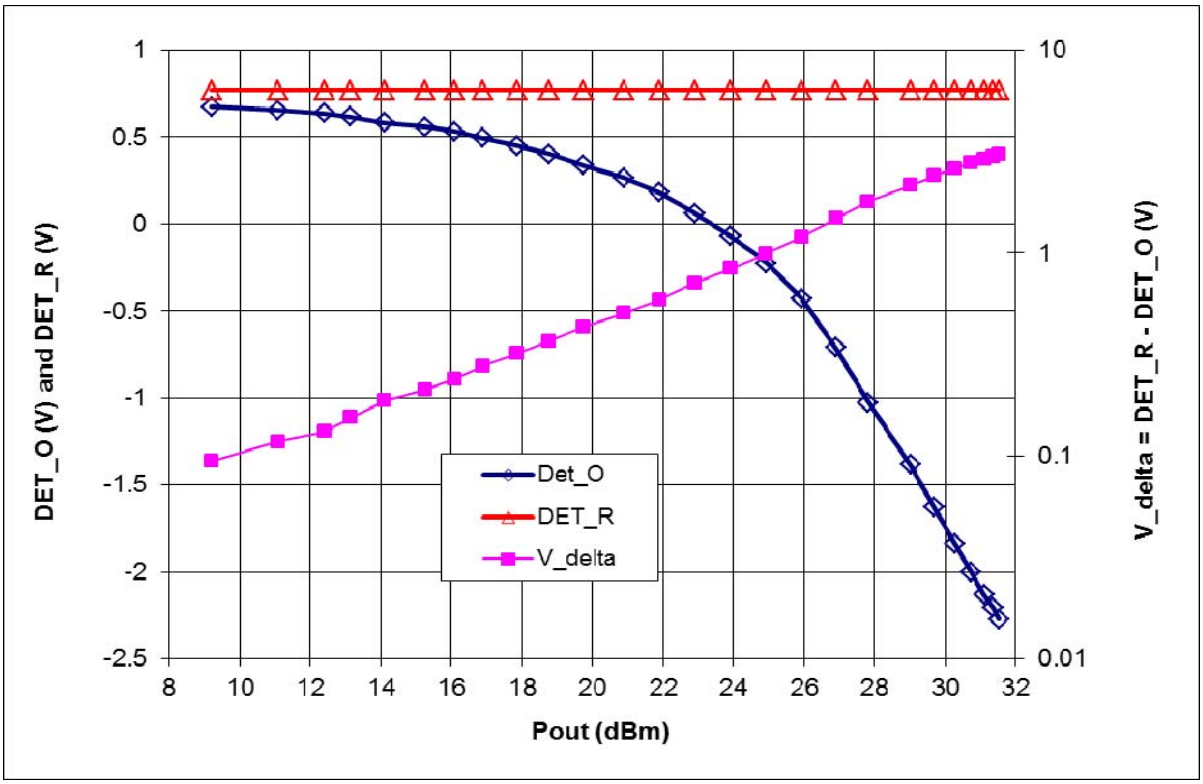
Typical Power Detector Voltages: Vds=5V, Idsq=1.2A, Frequency=39GHz



Detector Voltages (DET_O and DET_R) vs. Output RF power

Vdelta axis is Log-scale.

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Applications

The **MMA-374030-R5** MMIC power amplifier is designed for use as a power stage amplifier in microwave transmitters. It is ideally suited for 37 to 41GHz band point to point radio applications requiring a flat gain response and excellent linearity performance. This amplifier is provided as a 5x5mm QFN package, and the packaged amplifier is fully compatible with industry standard high volume surface mount PCB assembly processes.

Biasing and Operation

The recommended bias conditions for best performance for the **MMA-374030-R5** are VDD = 5.0V, Idsq = 1200mA. Performance improvements are possible depending on applications. For the best linearity performance, recommended DC bias conditions are VDD = 4.0V, Idsq = 1200mA. Drain bias voltage range is 4 to 6V and the quiescent drain current biasing range is 1000mA to 2000mA. A single DC gate supply connected to Vg will bias all the amplifier stages. Muting can be accomplished by setting Vg to the pinch-off voltage (Vp=-2V). The gate voltage (Vg) should be applied prior to the drain voltages (Vd1, Vd2, Vd3, Vd4) during power up and removed after the drain voltages during power down. The RF input and output ports are DC decoupled internally. Typical DC supply connection with bi-passing capacitors for the **MMA-374030-R5** is shown in following pages.

Assembly Techniques

GaAs MMICs are ESD sensitive. ESD preventive measures must be employed in all aspects of storage, handling, and assembly. MMIC ESD precautions, handling considerations, die attach and bonding methods are critical factors in successful GaAs MMIC performance and reliability.

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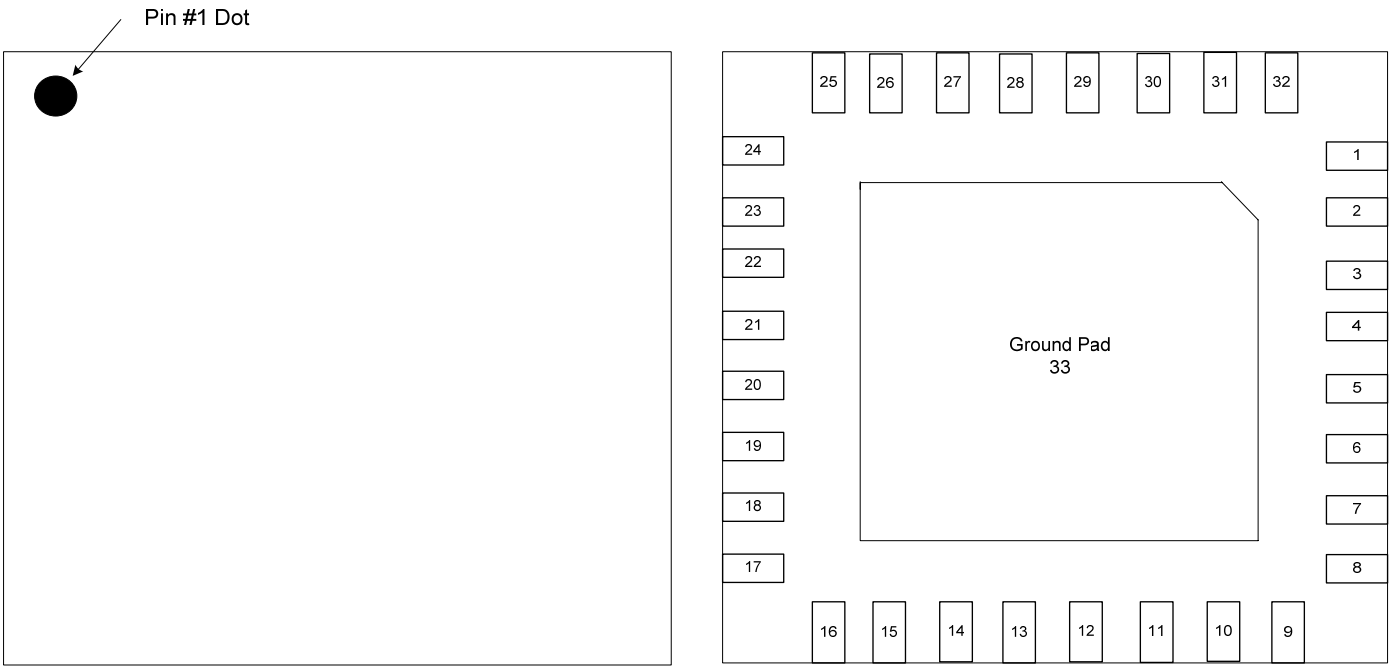
Package Pin-out:



Pin	Description
4	RF Input
21	RF Output
10	Vg
30	Vd1
29	Vd2
28	Vd3
14, 27	Vd4
23	DET_Reference
26	DET_Output
1, 3, 5, 8 ,9, 16, 17, 20, 22, 24, 25, 32, 33	Ground
2, 6, 7, 11, 12, 13, 15, 18, 19, 30, 31	N/C

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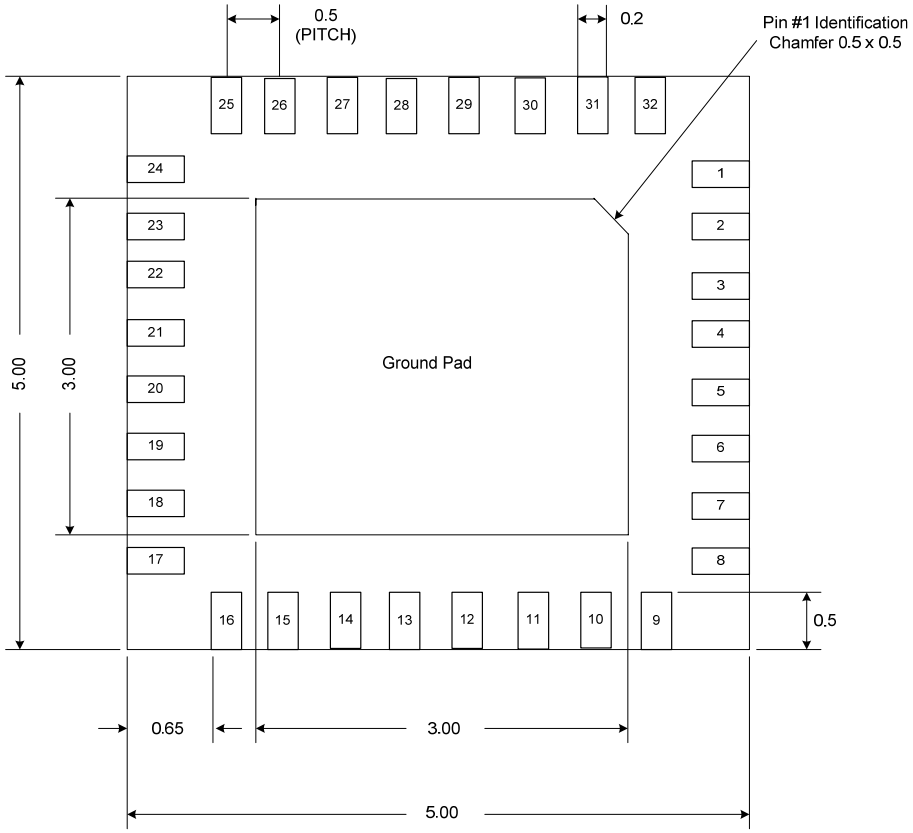
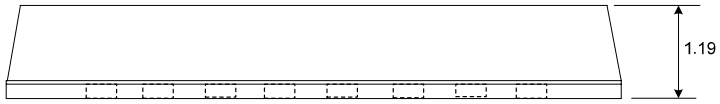
Mechanical Information:



BOTTOM VIEW



SIDE VIEW

The units are in [mm].

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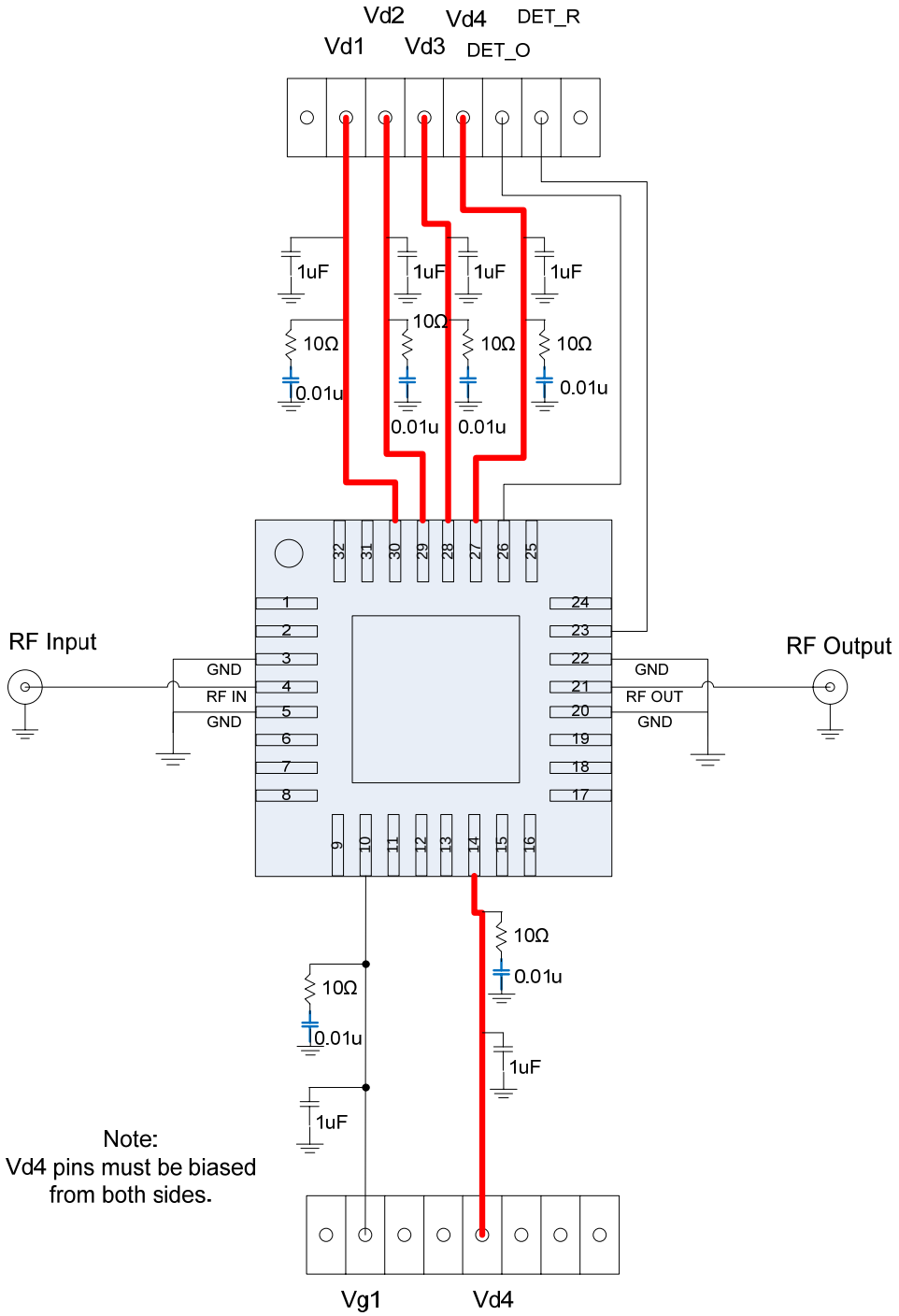
Application Circuit:



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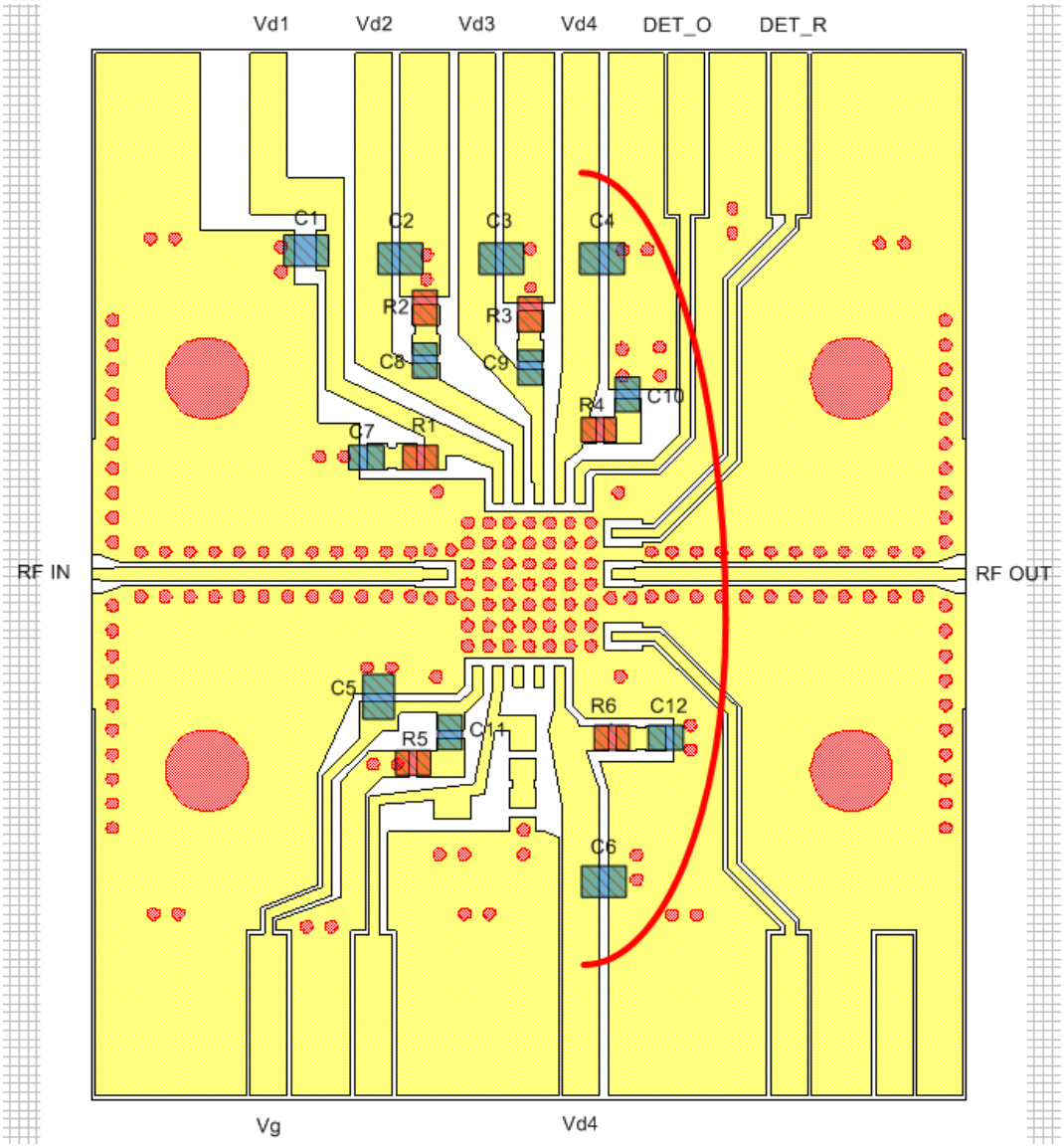
Recommended Application Board Design:

Board Material is 10mil (Dielectric) thickness Rogers 4350B with 0.5oz cupper clads.
Board is soldered on a gold plated solid cupper block and adequate heat-sinking is required for 16.8W total power dissipation.



Part	Description
C1, C2, C3, C4, C5, C6	1uF capacitor (0603)
C7, C8, C9, C10, C11, C12	0.01uF Capacitor (0402)
R1, R2, R3, R4, R5, R6	10Ω Resistor (0402)

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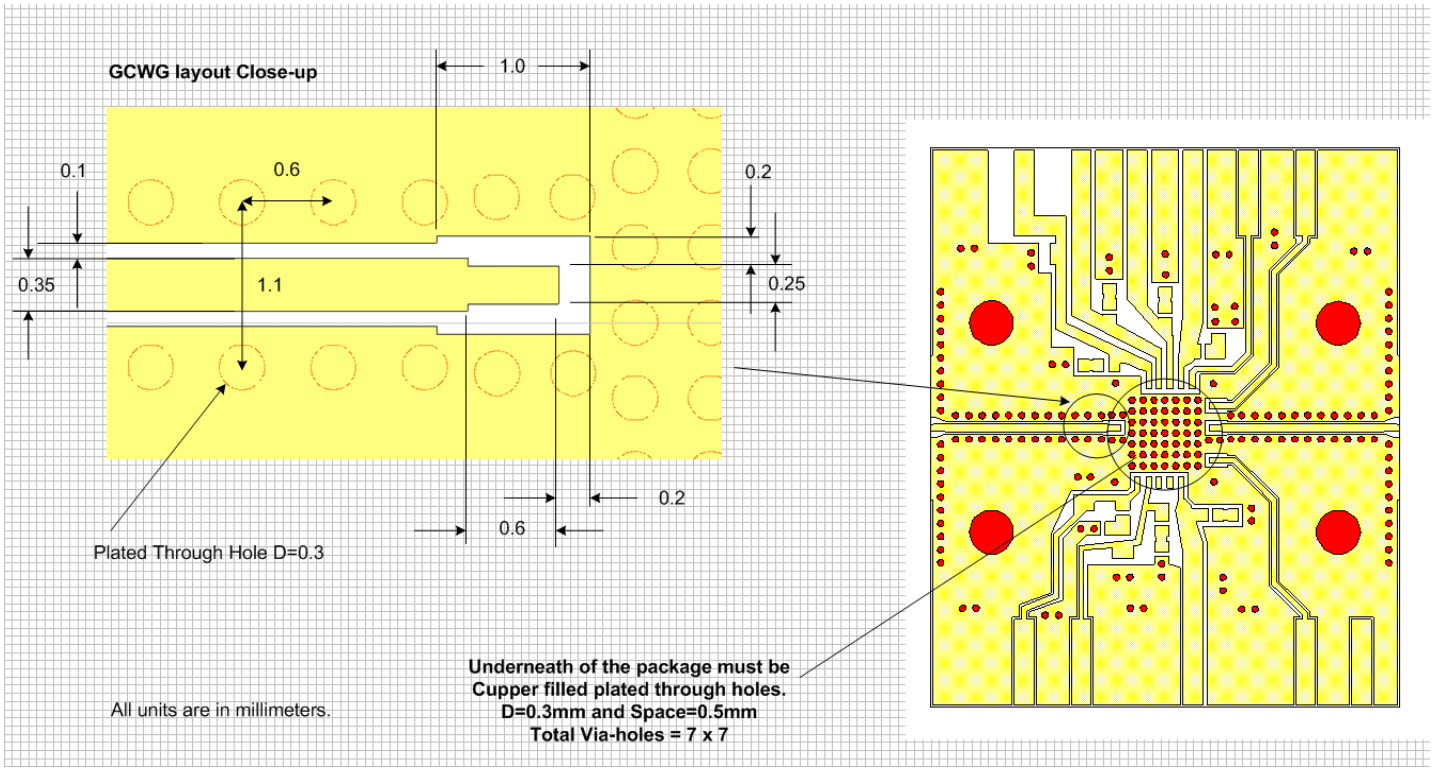
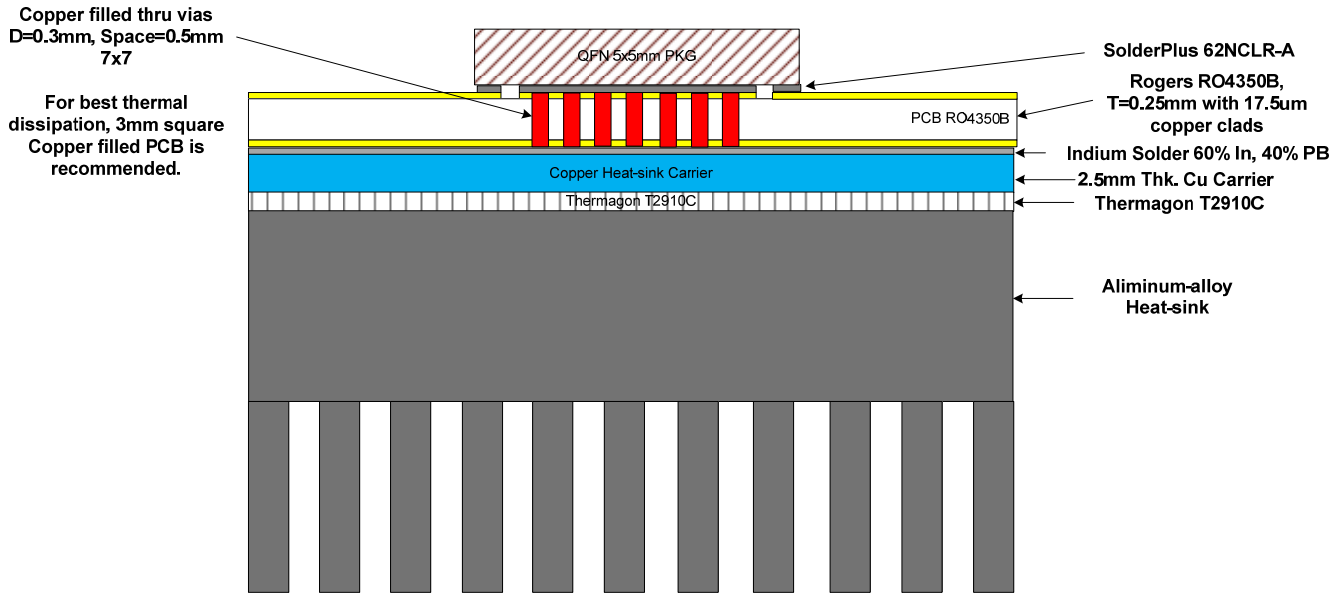
Recommended Application Board Design:

Board Material is 10mil (Dielectric) thickness Rogers 4350B with 0.5oz cupper clads. The board material and mounting pattern, as defined in the data sheet, optimizes RF performance and is strongly recommended. An electronic drawing of the land pattern is available upon request from *MwT* Sales & Application Engineering.





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