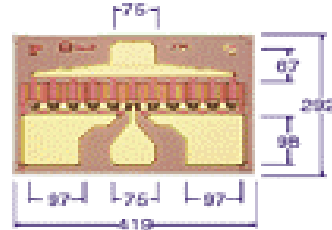


## Features:

- +27.0 dBm typical Output Power at 12 GHz
- 10.0 dB typical Small Signal Gain at 12 GHz
- 50 % typical PAE at 12 GHz
- 0.3 x 750 Micron Refractory Metal/Gold Gate
- Sorted into 12 mA Idss Bin Ranges
- Excellent for High Power, Gain, and High Power Added Efficiency Applications
- Ideal for Commercial, Military, Hi-Rel Space Applications



**Chip Dimensions: 419 x 292 microns**  
**Chip Thickness: 100 microns**

## Description:

The MwT-PH9 is a AlGaAs/InGaAs PHEMT (Pseudomorphic-High-Electron-Mobility-Transistor) device whose nominal 0.3 micron gate length and 750 micron gate width make it ideally suited for applications requiring high-gain and power up to 26 GHz frequency range with power outputs ranging from 400 to 500 milli-watts. The device is equally effective for either wideband (e.g. 6 to 18 GHz) or narrow-band applications. The chip is produced using MwT's reliable metal systems and all devices from each wafer are screened to insure reliability. All chips are passivated using MwT's patented "Diamond-Like Carbon" process for increased durability.

## Electrical Specifications:

• at  $T_a = 25\text{ }^\circ\text{C}$

SYMBOL	PARAMETERS & CONDITIONS	FREQ	UNITS	MIN	TYP
<b>P1dB</b>	Output Power at 1dB Compression Vds=7.0 V Ids=0.6xIDSS=150 mA	12 GHz	dBm	26.0	27.0
<b>SSG</b>	Small Signal Gain VDS=7.0 V Ids=0.6xIDSS=150 mA	12 GHz	dB	9.0	10.0
<b>PAE</b>	Power Added Efficiency at P1dB VDS=7.0 V Ids=0.6xIDSS=150 mA	12 GHz	%		50
<b>IDSS</b>	Recommended IDSS Range for Optimum P1dB		mA		120- 292



**DC Specifications:** • *at Ta= 25 °C*

SYMBOL	PARAMETERS & CONDITIONS	UNITS	MIN	TYP	MAX
<b>IDSS</b>	Saturated Drain Current Vds=4.0 V Vgs=0.0 V	mA	144		318
<b>Gm</b>	Transconductance Vds=2.5 V Vgs=0.0 V	mS	150	200	
<b>Vp</b>	Pinch-off Voltage Vds=3.0 V Ids=5.0 mA	V		-1.2	-2.5
<b>BVGSO</b>	Gate-to-Source Breakdown Voltage Igs= -1.0 mA	V	-6.0	-12.0	
<b>BVGDO</b>	Gate-to-Drain Breakdown Voltage Igd= -1.0 mA	V	-10.0	-13.0	
<b>Rth</b>	Chip Thermal Resistance	°C/W		56*	

\* Overall Rth depends on case mounting

**MAXIMUM RATINGS AT Ta = 25 °C**

Symbol	Parameter	Units	Cont Max1	Absolute Max2
VDS	Drain to Source Volt.	V	7.5	8.0
Tch	Channel Temperature	°C	+150	+175
Tst	Storage Temperature	°C	-65 to+160	+180
Pin	RF Input Power	mW	240	360
Pt	Total Power Dissipation	mW	2700	3300

**Notes:**

1. Exceeding any one of these limits in continuous operation may reduce the mean-time- to-failure below the design goal.
2. Exceeding any one of these limits may cause permanent damage.

**ORDERING INFORMATION:**

When placing order or inquiring, please specify BIN range, wafer number, if known, and visual screening level required. For details of BIN Selection and Safe Handling Procedure please see supplementary information in available PDF on our website [www.mwtinc.com](http://www.mwtinc.com).

**BIN SELECTION**

<b>MwT-PH9</b>	<b>Old Bin</b>	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	<b>6</b>	<b>7</b>	<b>8</b>	<b>9</b>	<b>10</b>	<b>11</b>	<b>12</b>	<b>13</b>	<b>14</b>	<b>15</b>
	<b>New Bin</b>	A	A	A	B	B	B	C	C	C	C	C	C	D	D	D
	<b>Idss</b>	114-	126-	138-	150-	162-	174-	186-	198-	210-	222-	234-	246-	258-	270-	282-
	<b>Range</b>	126	138	150	162	174	186	198	210	222	234	246	258	270	282	294