

Biasing and Operation

The recommended bias conditions for optimum performance for the **MMA-243034D-M5** are $V_{DD} = 6.0V$, $I_{dq} = 1600mA$. The gate voltage (V_g) must be applied prior to the drain voltages (V_{d1} , V_{d2} , V_{d3} , V_{d4}) during power up and removed after the drain voltages during power down. A single DC gate supply connected to V_g will bias all the amplifier stages. Muting can be accomplished by setting V_g to the pinch-off voltage ($V_p = -2V$). V_{d4} must be connected to both V_{d1} pins.

Assembly and Handling

GaAs MMICs are ESD sensitive. ESD preventive measures must be employed in all aspects of storage, handling, and assembly.

Sample Application Board Design:

Proper heatsinking and board mounting pattern with filled thermal vias are recommended for optimum performance. An electronic drawing of the sample board layout is available upon request from *MwT* Sales & Application Engineering.

